

REMARKS

Claims 1, 3-13, 15, 24, 36, 37, 39, 41 and 43 are presently pending. Claims 2, 14, 16, 18-23, 25-35, 38, 40 and 42 have been cancelled. Claims 12, 24, 26 and 39 stand rejected. Claims 17 and 43 are objected to. Claims 1, 3-13, 36, 37 and 41 are allowed. Claim 15, 24 and 39 have been amended.

Claims 17 is objected to as being dependent from rejected base claim 15, but would be allowable if rewritten in independent form. Claim 15 has been amended to incorporate the limitations of claim 17. Claim 17 has been cancelled.

Claim 43 is objected to because the words "said output" on line 9 are duplicated. Applicant respectfully submits that no correction is necessary. Claim 43 recites, *inter alia*, "a plurality of charge pump circuits responsive to said output, said output activating more than one of said plurality of charge pump circuits responsive to said sensing circuit." The first phrase describes how the charge pump circuits are responsive to the output; the second phrase described how activation of the charge pump circuits by the output is responsive to the sensing circuit. Applicant respectfully requests that this objection be withdrawn.

Claim 15 stands rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 6,348,886 to Frank et al. This rejection is respectfully traversed.

Claim 15 has been amended to include all the limitations of claim 17, which the Office Action has indicated at page 4 would be allowable if rewritten in independent form. Applicant respectfully requests that the rejection be withdrawn.

Claim 24, 26 and 39 stand rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 5,448,598 to Yousefi et al. This rejection is respectfully traversed.

Claim 24 recites a method of controlling a power supply comprising, *inter alia*, “adapting [a] power supply to supply a particular level of current, said level of current being proportional to [a] number of bits by switchingly connecting a plurality of power supply portions to a load circuit, each power supply portion comprising a charge pump circuit and a respective clock generation circuit, each of said clock circuits operatively connected to, and adapted to drive, a respective charge pump circuit, wherein said act of supplying a particular level of current comprises activating more than one of said plurality of power supply portions.”

Yousefi does not disclose “clock circuits operatively connected to, and adapted to drive, a respective charge pump circuit, wherein said act of supplying a particular level of current comprises activating more than one of [a] plurality of power supply portions” as recited in the claim. Applicant respectfully requests that the rejection be withdrawn.

Claim 26 has been cancelled.

Claim 39 recites a power supply controller comprising, *inter alia*, a “plurality of charge pump circuits being adapted to modify [a] power output by activating more than one of said plurality of charge pump circuits.”

Yousefi does not disclose “charge pump circuits being adapted to modify [a] power output by activating more than one of said plurality of charge pump circuits” as recited in the claim. Applicant respectfully requests that the rejection be withdrawn.

Applicant agrees in part with the Examiner's statement of reasons for allowance of claims 1, 3-13, 17, 36, 37, 41 and 43, in that the prior art of record fails to disclose, teach or suggest the claimed invention. However, Applicant notes that the Examiner's statement fails to address all limitations of all of the allowed claims. For example, certain claim limitations were not discussed in the Examiner's Reasons for Allowance, including those in most of the allowed claims, each of which defines a unique combination of features not shown or suggested by the prior art, providing additional reasons for allowance of each claim.

In view of the above amendment, applicant believes the pending application is in condition for allowance.

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Respectfully submitted,

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